GHARDA FOUNDATION'S

GHARDA INSTITUTE OF TECHNOLOGY

FACULTY PROFILE

of

Dr. Sachin R. Khedekar

(Department of Electronics & Telecommunication Engineering)

PERSONAL PROFILE

Full Name	:	Dr. Sachin Ramesh Khedekar
Highest Educational Qualification	:	Ph.D.
Permanent address	:	B2-209, Second floor, GreenCove, Paranjape schemes, Pagmala, Chiplun
Contact telephone numbers	:	8379833523,9403573999
E-mail id	:	hodextc@git-india.edu.in, srkhedekar@git-india.edu.in
Current designation	:	HOD (Electronics & Telecommunication Engg.)
No. of Research papers	:	14
Books published	:	--
Number of Conferences / Workshops / STTPs conducted	:	02
Number of Conferences / Workshops / STTPs attended	:	20
Syllabus workshops	:	02
Patents/Copyright published	:	01

EDUCATIONAL QUALIFICATION

Degree	Specialization	Year of passing	Institute/University	Percentage
Ph.D.	Electronics & comm.	2020	Birla Institute of Technology, Mesra, Ranchi	NA
M.E.	Electronics & Telecomm.	2008	KIT's College of Engineering, Kolhapur	62.78%
B.E.	Electronics & Telecomm.	2000	NMU Jalgaon	60.66
MSBTE Diploma	Electronics & Telecomm	1996	Government Polytechnic, Ratnagiri	63.45

RECOGNITIONS / APPRECIATIONS RECEIVED

Sr. No.	Title	Awarded by	Date	Remarks
1	Recognition for guiding project "Voice based help desk"	Intercollegiate research convention,Mumbai University	December 15, 2023	--
2	Worked as a mentor for online course on 'Python for data science'	Swayam NPTEL	July - Dec 2022	
3	Huawei Certified Academic Instructor	Huawei	Sept 28, 2020	--

RESEARCH PAPER(S) /BOOKS PUBLISHED

PAPERS PUBLISHED IN REFFERED JOURNALS

Publication	Title of paper	Year of Publication
Wireless Personal Communications (2024) 135:967–978 https://doi.org/10.1007/s11277-024-11098-z (Scopus, SCI indexed)	Design approach and comparison of phase shifter performance in DOA estimation system	2024
Journal of Engineering Technology, vol. 7, pp. 62-73, Oct. 2018.(SCI-E)	Implementation of One Time Pad based encryption: Way to unbreakable encryption and Introduction of Pseudo OTP generation	2018

Elsevier Materials Today Proceedings, pp. 1696–1703, 5, 2018. (SCOPUS)	Analysis of Estimation of Direction of Arrival by Comparative Study	2018
International Journal of Control Theory and Applications, vol. 9(2), pp. 509-518, 2016.(SCOPUS)	DOA by Antenna Array Plane Rotation Minimizes Antenna Elements and Phase	2016
International Journal of Current Engineering and Scientific Research, vol. 1(2), pp. 21-29, 2014.	Implementation of a proposed 2D DOA Estimation Algorithm on an FPGA platform	2014
International Journal of Advanced Engineering and Global Technology, vol. 2(12), pp. 187-193, 2014.	Interference Mitigation through Adaptive Array Processing in GPS	2014
International Journal of Emerging Technologies and Applications in Engineering, Technology and Sciences), special issue, pp. 14-20,2014.	Direction of Arrival Estimation by Mechanically rotating Adaptive Array Antenna Plane	2014

PAPER PRESENTED AT CONFERENCES (National/International)

Sr. No.	Conference	Title	National/ International	Location	Year
1	IEEE International Conference on Signal Processing, Communication, Power and Embedded System	Digital Beamforming to Reduce Antenna Side Lobes and Minimize DOA Error	International	Paralakhemundi, Odissa, India	2016
2	International conference (ICETAETS)	New Approach for CMMI & PMI	International	Saurashtra University, Rajkot, Gujrat	2008
3	International conference (ICETAETS)	Automatic Speaker Recognition using MFCC & VQ	International	Saurashtra University, Rajkot, Gujrat	2008
4	MERG 07	Internet Cryptography using Firewall System	National	Department of Computer Engineering Smt. KashibaiNavale College of Engineering Vadgaon (Bk.), Pune (Maharashtra)	2007
5	National conference	Phased based Matching Technique for Iris Recognition	National	Pusad (Maharashtra)	2007

6	International conference (CITICOMS)	ASR: Speech Feature Extraction & Matching Technique	International	Coimbatore Institute of Technology, Coimbatore, Tamilnadu	2007
7	International conference (ICON ADELCO)	Mel Frequency Cepstrum Coefficient Processor	International	National Engineering College Kovilpatti, Tamilnadu	2007

BOOK PUBLISHED

Name of the Book	Publisher	ISBN No.	Year
--			

NATIONAL AND INTERNATIONAL CONFERENCES / WORKSHOPS / STTPS CONDUCTED
TECHNICAL WORKSHOP/STTP/SEMINARS

Sr. No.	Name of the conference/workshop	Place	Duration
1	1 day workshop on esim software	GIT Lavel	27th July 2019
2	1 week STTP on Artificial Intelligence and Machine Learning	GIT Lavel	Jan 6,2025 to Jan 11, 2025

CONTRIBUTIONS AT UNIVERSITY LEVEL:
SYLLABUS WORKSHOPS

Sr. No.	Year	Name of the college	Duration	Role Played
1.	2009	Terna Engineering College, Nerul, Mumbai (MS)	08th July, 2009	Subject expert for Microprocessors (TE comp)

| 2 | 2020 | LokmanyaTilak College of Engineering,KoparKhairane,Navi Mumbai" on behalf of University of Mumbai | 13th July 2020 | Expert for Mini Project 1 a (ECM301) |

SPECIAL EFFORTS TAKEN IN IMPROVING TEACHING-LEARNING PROCESS

Sr. No.	Name of the Course	Place	Duration
1	Microprocessors and peripheral Interfacing (Audio and video recording of all lectures)	GIT Lavel	July 2020 to Nov 2020

NATIONAL AND INTERNATIONAL CONFERENCES / WORKSHOPS / STTPS ATTENDED
TECHNICAL WORKSHOP/STTP/SEMINARS

Sr. No.	Title	Place	Duration
1	ATAL AICTE FDP on AI Revolution: From Machine Learning to Generative AI by YBIT Sawantwadi	Online	Jan 6, 2025 to Jan 11, 2025
2	STTP on Emerging Significance of Artificial Intelligence inBiomedical, Industry 5.0,Cyber Security & Data Forensics by VPM's Maharshi Parshuram College ofEngineering, Velneshwar	Online	August 26, 2024 to August 31, 2024
3	STTP on Inculcating Universal Human Values in Technical Education (AICTE approved)	GIT Level	July 01, 2024 to July 03, 2024
4	Computer Network & Internet Protocol	NPTEL	Jan 2024 to Apr 2024 (12 weeks)
5	NEP 2020 Orientation & Sensitization Programme under the Malaviya Mission Teacher Training Programme (MM-TTP) of the University Grants Commission (UGC)	Online	December 18, 2023 to December 27, 2023
6	IETE approved STTP on 'Recent trends in Cloud Computing and its Security' by Enterprise and Information Security Research group, Department	Online	July 04, 2022 to July 09, 2022

	of Computer Engineering, RAIT, Nerul, Navi Mumbai		
7	'IoT' sponsored by AICTE Atal academy	NITK Surathkal, Karnataka	4th 2019 to November 8th 2019
8	'eSim' under the scheme PMMMNMTT MHRD Govt. of India	GIT Lavel	27th July 2019
9	'Scilab' under the scheme PMMMNMTT MHRD Govt. of India (IIT Bombay)	GIT Lavel	5th May 2019
10	'Moodle Learning Management System' under the PMMMNMTT MHRD Govt. of India (IIT Bombay)	GIT Lavel	15th March 2019
11	Two week ISTE STTP on 'Electric Power System' (IIT Kharagpur)	GIT Lavel	10th July – 15th July, 2017
12	two Week ISTE STTP on 'CMOS, Mixed Signal and Radio Frequency VLSI Design'(IIT Kharagpur)	GIT Lavel	30th January to 4th February, 2017
13	'Technical Communication " under the national mission on education through ICT (MHRD, Govt. of India)(IIT Kharagpur)	GIT Lavel	30th November 2015 to 05 th December 2015
14	Seminar on 'MATLAB' organized by EXTC dept GIT Lavel	GIT Lavel	28th September 2015
15	'Control Systems" under the national mission on education through ICT(MHRD, Govt. of India)(IIT Kharagpur)	GIT Lavel	02th December to 12 December 2014
16	Embedded System Design	Sinhgad Institute of Technology, Pune	10th May 2010 to 22nd May 2010
17	Signal Processing & Applications	Vidyalank Institute Technolo Wadala, Mumbai (	06th July to 12th July, 2009
18	Applications of fuzzy logic &Matlab in Engineering	J.J. Mag College Engg. Jaysingpu S)	15th December to 26th December, 2008

		Coimbatore Institute of Technology, Coimbatore, Tamilnadu	27-29 August, 2007
19	International conference (CITICOMS)	Coimbatore Institute of Technology, Coimbatore, Tamilnadu	27-29 August, 2007
20	International conference (ICON ADELCO)	National Engineering College Kovilpatti, Tamilnadu	1-3 Feb 2007

CONTRIBUTIONS AT UNIVERSITY LEVEL:

WORKED AS EXAMINER / CHAIRMAN/ FOR BOARD OF STUDIES

Sr. No.	Name of the Course	Place	Duration
1	Examiner for Computer Communication Network course	GIT Laval/FAMT Ratnagiri	2 days
2	Examiner for 'Microprocessors and Peripheral Interfacing'	GIT Laval	1 Day
3	Examiner for 'Telecom Network Management'	GIT Laval	1 Day
4	Examiner for 'Data Compression & Encryption'	GIT Laval	1 Day
5	Examiner for 'Network Management in Telecom.'	GIT Laval	1 Day

MEMBERSHIP OF PROFESSIONAL BODIES

Sr. No.	Professional Body	Nature of Membership	Date
1	ISTE	Lifetime membership (LM50878)	2007
2	IETE	Institute membership	

EXTRA CURRUCULAR ACTIVITIES / ORGANISING COMMITTEE
EXTRA CURRICULAR ACTIVITIES

Sr. No.	Name of the activity	Name of College	Date	Played
1	Techfest	GIT Lavel	2018	Judge for paper presentation

ORGANISING COMMITTEE

Sr. No.	Name of the activity	Name of College	Duration
1.	Technical Festival (Paper presentation, Project presentation)	GIT Lavel	2 days

WORKED AS RESOURCE PERSON: TAUGHT SUBJECTS & WORKED AS JUDGE

Sr. No.	Year	Subject	Name of College	Duration
1.	2015	Judge for paper presentation competition	SSPM Kankawali	1 day

ANY OTHER SIGNIFICANT INFORMATION

1. Worked as a local Inquiry Committee (LIC) member for University of Mumbai.

2. Worked as a coordinator for Marathi Vidnyan Parishad program held GIT Lavel.

3. Worked as a cluster head (Electronics & Telecommunication engineering department) for conducting examination in online mode for Mumbai University during COVID pandemic.

Prof. Dr. Sachin R. Khedekar

Prof. Dr. Sachin R. Khedekar